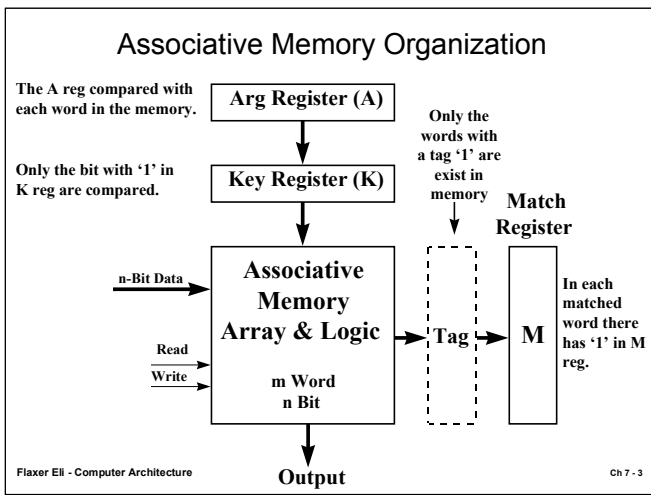


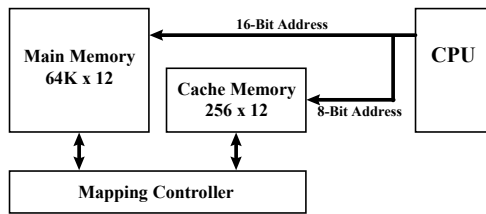
Address Map Table

| | BEGIN | END | A ₁₂ | A ₁₁ | A ₁₀ |
|--------------|--------------|--------------|-----------------|-----------------|-----------------|
| RAM 0 | 0000H | 03FFH | 0 | 0 | 0 |
| RAM 1 | 0400H | 07FFH | 0 | 0 | 1 |
| RAM 2 | 0800H | 0BFFH | 0 | 1 | 0 |
| RAM 3 | 0C00H | 0FFFH | 0 | 1 | 1 |
| ROM | 1000H | 1FFFH | 1 | x | x |

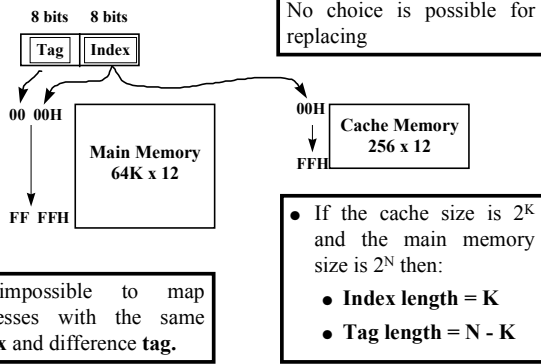
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Example



Direct Mapping



Direct Mapping

| Main Memory Address (16 Bit) | Main Memory Data 12 Bit | Cache Address | Cache Memory Tag | Cache Memory Data |
|------------------------------|-------------------------|---------------|------------------|-------------------|
| 0000H | 123H | 00 | 00 | 123H |
| 00FFH | 234H | FF | 00 | 234H |
| 0100H | 345H | 00 | 01 | 345H |
| 01FFH | 456H | FF | 01 | 456H |
| 0200H | 567H | 00 | 02 | 567H |
| 02FFH | 678H | FF | 02 | 678H |
