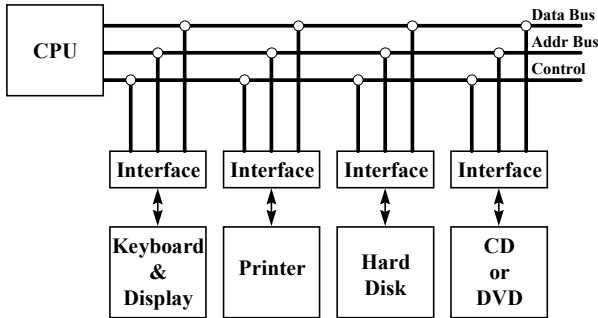


## I/O Bus and Interface




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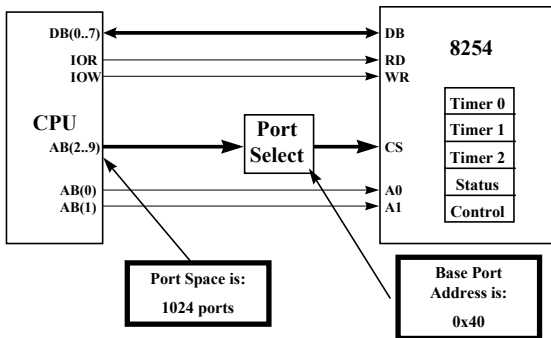
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## Example: 8254




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## 8254 Control Field

SC - Select Counter		
SC1	SC0	
0	0	Select Counter 0
0	1	Select Counter 1
1	0	Select Counter 2
1	1	Read - Back Command

RW - Read Write		
RW1	RW0	
0	0	Counter Latch Command
0	1	Read / Write LSB
1	0	Read / Write MSB
1	1	Read / Write LSB first & MSB after

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## 8254 Control Field

M - Mode			
M2	M1	M0	
0	0	0	Mode 0
0	0	1	Mode 1
0	1	0	Mode 2
0	1	1	Mode 3
1	0	0	Mode 4
1	0	1	Mode 5

BCD - Binary Code Decimal	
BCD	
0	Binary Counter 16 Bit
1	BCD Counter 4 Digit

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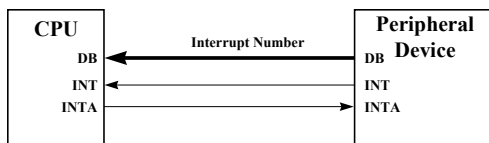
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## Hardware Interrupt Procedure



Interrupt Vector	Absolute Address	Opcode (Method 1)	Jump Address (Method 2)
0	0	Jmp Sub0	Address of Sub0
1	1	Ret	Address of Ret Code
2	2	Jmp Sub2	Address of Sub2
*	*	Ret	Address of Ret Code
*	*	Ret	Address of Ret Code
15	15	Ret	Address of Ret Code

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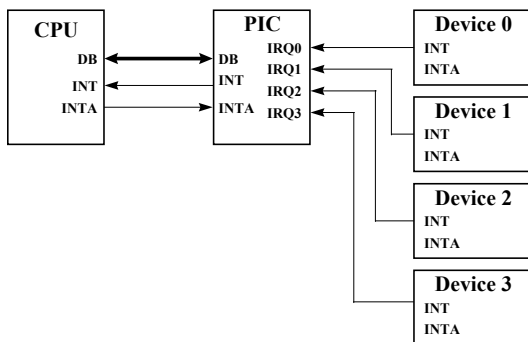
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## Multi Interrupts Procedure




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