

## INDEX ADDRESS OF IODrv2000

### Address

0	= ADCL	- Read low byte	;ADC all Channel
1	= ADCH	- Read high byte	;ADC all Channel
2	= ADCS	- Write = Control & start conversion, Read = INT end of conversion.	
3	= RESERV	- Optional	
4	= DAC0L	- Load low byte	;Analog Out Chan 0
5	= DAC0H	- Load high byte and update	;Analog Out Chan 0
6	= DAC1L	- Load low byte	;Analog Out Chan 1
7	= DAC1H	- Load high byte and update	;Analog Out Chan 1
8	= DIGITAL	- Write = Digital Out, Read=Digital In.	
9	= PWM0	- Load Duty Cycle byte for PWM Channel 0 Bits [1,2].	
10	= PWM1	- Load Duty Cycle byte for PWM Channel 1 Bits [3,4].	
11	= CONTROL	- Enable or Disable the PWM0[1,2], PWM1[3,4], CNT1, CNT2 and Timer.	
12	= TIMER	- Write 4 Timer Division Nibbel.	
13	= CNTC	- Write Latch & Reser Counters.	
14	= LATCH_L	- Read Latch low byte.	
15	= LATCH_H	- Read Latch High byte.	

Read from ADC L is analog to digital channel (8 low bit).

Read from ADC H is analog to digital channel (4 high bit).

Write to ADC S (control byte) is to start conversion A/D.

Read from ADC S (bit 0) is INT- end of conversion A/D.

Write to DACx L is digital to analog channel x 8 low bit.

Write to DACx H is digital to analog channel x 4 high bit + UpDate.

CONTROL

R/W	7	6	5	4	3	2	1	0
W	BUZ	CNT3e	CNT2e	CNT1e	PWM4e	PWM3e	PWM2e	PWM1e

CNTC

R/W	7	6	5	4	3	2	1	0
W					LAC2	LAC1	XRST2	XRST1